

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Docket No.:	TI-35434
Gerard Chauvel	Examiner:	Petranek, J. A.
Serial No.: 10/631,246	Art Unit:	2183
Filed: July 31, 2003	Conf. No.:	1610
For: Processor That Accommodates Multiple Instruction Sets And Multiple Decode Modes		

INFORMATION DISCLOSURE STATEMENT

February 4, 2008

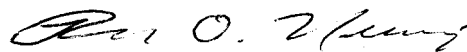
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Search Report received from the European Patent Office (a copy of which is enclosed), Applicant wishes to bring to the attention of the U.S. Patents and Trademarks Office the information noted on the enclosed PTO/SB/08A. Copies of the cited references are enclosed. All of these references were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to filing this statement.

No fee is believed by Applicant to be due; however, the Commissioner is hereby authorized and requested to charge any necessary fee to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



/Ronald O. Neerings/

Attorney for Applicants

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